

<https://www.nist.gov/chips/workforce-development>



## **CHIPS FOR AMERICA** (<https://www.nist.gov/chips>)

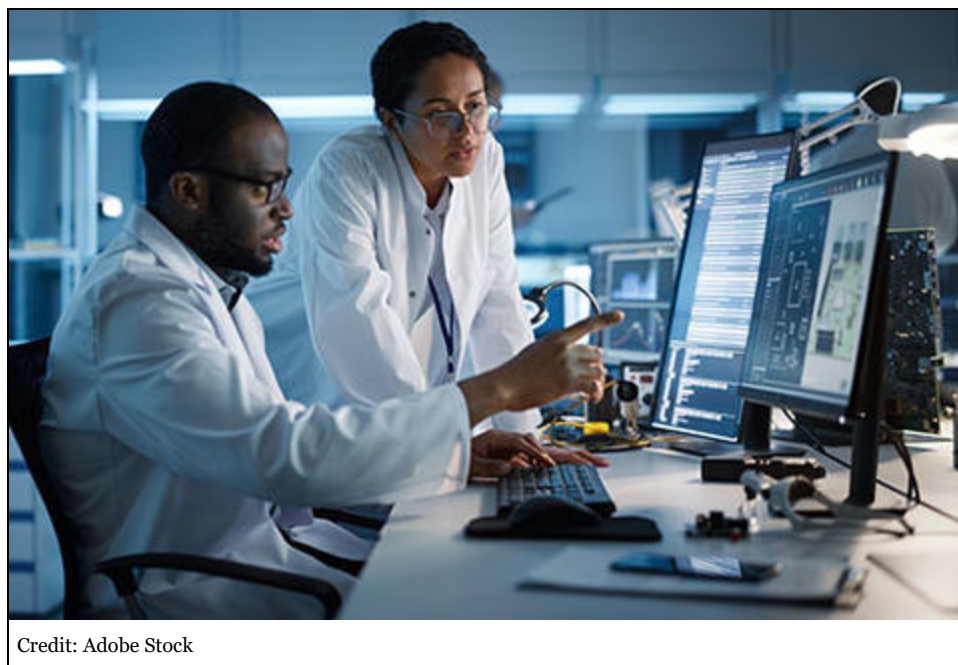
# Workforce Development

## Resources:

- Davis-Bacon and Related Acts: General Contact List (<https://www.nist.gov/document/davis-bacon-and-related-acts-general-contact-list>)
- Davis-Bacon and Related Acts and the CHIPS and Science Act: Frequently Asked Questions (<https://www.nist.gov/document/davis-bacon-and-related-acts-and-chips-and-science-act-frequently-asked-questions>)
- Building the U.S. Semiconductor Workforce (<https://www.nist.gov/document/chips-america-workforce-progress-report>): Progress Report from CHIPS for America (PDF)
- CHIPS for America Teaming Partner List (<https://www.nist.gov/chips/chips-america-teaming-partner-list>) - An access point for entity collaboration.
- CHIPS for America Women In Construction Guide (<https://www.nist.gov/document/chips-america-women-construction-guide>) for CHIPS Incentive partners (PDF)
- CHIPS for America Workforce Development Planning Guide (<https://www.nist.gov/document/workforce-development-planning-guide>) for CHIPS Incentives applicants (PDF)
- Project Labor Agreements 101 (<https://www.youtube.com/watch?v=sqsJvvtP96g>) - A webinar from the U.S. Department of Labor
- CHIPS for America Fact Sheet: Building a Skilled and Diverse Workforce (<https://www.nist.gov/document/chips-workforce-development-fact-sheet>)
- CHIPS for America Fact Sheet: Principles for Engagement With CHIPS Applicants During Due Diligence Phase To Ensure A Skilled And Diverse Workforce (<https://www.nist.gov/document/due-diligence>)
- CHIPS for America Fact Sheet: Engaging Strategic Partners to Build a Skilled and Diverse Workforce (<https://www.nist.gov/document/engaging-strategic-build-skilled-and-diverse-semiconductor-workforce>)

- **CHIPS for America Fact Sheet: Supporting Regional Semiconductor Industry Growth through Workforce Partnerships**  
(<https://www.nist.gov/document/intermediary-fact-sheet>).

A skilled and diverse pipeline of workers is critical to building a sustainable domestic semiconductor industry and to achieving the CHIPS Act economic and national security goals. As such, workforce development is a priority across both the CHIPS Incentives and **Research and Development** (<https://www.nist.gov/chips/research-and-development-programs>) programs. Specifically, the CHIPS Act investments will build a domestic semiconductor workforce by:



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- **Investing in Manufacturing Facilities:** Provide funding for the construction, expansion, and modernization of manufacturing facilities which will create new job opportunities for construction workers, technicians, engineers, and other occupations at multiple skill levels.
- **Partnering with Industry and Education and Training Providers:** Collaboration between education and training providers and the semiconductor industry will ensure that education and training programs are aligned with industry needs and that students are well-prepared to fill in-demand roles.
- **Supporting Semiconductor Education and Training:** Support semiconductor-related skills development throughout the entire education and training system—from K-12 schools and career technical education programs through community colleges and universities. Provide experiential and practical learning opportunities for students and researchers. Offer supportive services to improve access to education and training opportunities for all Americans and increase diversity in the semiconductor workforce.

- **Fueling Research and Development:** Increased funding for semiconductor R&D will lead to the discovery and development of new products and technologies. This will help to create more job opportunities and help to attract more workers to the field.

The CHIPS Act offers a unique and historic opportunity to expand the nation's capacity for semiconductor production as well as to prepare the American workforce for good jobs in advanced manufacturing and engineering.

The success of CHIPS for America will require collaboration between businesses, governments, education and training providers, economic and workforce development organizations, unions, community-based organizations, and other supporting organizations to help recruit, train, hire, and retain a highly-skilled semiconductor and construction workforce. CHIPS investments will create good jobs in communities around the country and expand economic opportunities for individuals and populations from historically underserved communities, including women, people of color, workers in rural areas, and veterans.

Learn more about workforce development and the first CHIPS Notice of Funding Opportunity from this [fact sheet](https://www.nist.gov/document/chips-workforce-development-fact-sheet) (<https://www.nist.gov/document/chips-workforce-development-fact-sheet>).

[Electronics](https://www.nist.gov/topic-terms/electronics) (<https://www.nist.gov/topic-terms/electronics>), [Semiconductors](https://www.nist.gov/topic-terms/semiconductors) (<https://www.nist.gov/topic-terms/semiconductors>), [Information technology](https://www.nist.gov/topic-terms/information-technology) (<https://www.nist.gov/topic-terms/information-technology>), [Manufacturing](https://www.nist.gov/topic-terms/manufacturing) (<https://www.nist.gov/topic-terms/manufacturing>) and [Materials](https://www.nist.gov/topic-terms/materials) (<https://www.nist.gov/topic-terms/materials>).

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