

---

**From:** liz lee <liz.lee@oracle.com>  
**To:** Dumont, Chantal <chantal.dumont@oracle.com>; Les.cundall@oracle.com <Les.cundall@oracle.com>  
**Sent:** 9/24/2013 12:16:26 AM  
**Subject:** Fwd: Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [REDACTED] of CMU

Screven

----- Original Message -----

**Subject:** Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [REDACTED] of CMU  
**Date:** Fri, 20 Sep 2013 08:54:25 -0700  
**From:** Craig Stephen  
**To:** liz lee  
**CC:** Eric Sedlar , Nipun Agarwal , Rita Ousterhout

I approve. In FY14 budget for project RAPID.

Craig

On Sep 20, 2013, at 8:26 AM, liz lee <liz.lee@oracle.com> wrote:

Hi Craig,

Please approve the following offer and return to me as soon as possible.

Thank you,

Liz

On 9/19/2013 7:48 PM, Eric Sedlar wrote:

Approved.

On 9/19/2013 6:23 PM, Nipun Agarwal wrote:

Approved.

Nipun

On Sep 19, 2013, at 5:31 PM, liz lee <liz.lee@oracle.com> wrote:

Hi Nipun,

Please approve the following offer and return to me as soon as possible.

Thank you,

Liz

On 9/19/2013 5:31 PM, Rita Ousterhout wrote:

Approved

---

**From:** liz lee

Sent: Thursday, September 19, 2013 5:28 PM

To: Rita Ousterhout

Subject: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [REDACTED] of CMU

Hi Rita,

Please approve the following offer and return to me as soon as possible.

Thank you,

Liz

Candidate: [REDACTED]  
Title: Senior Member of Technical Staff (10530)  
Group: [REDACTED]  
Hiring Manager: [REDACTED]  
Salary: \$ [REDACTED]  
Relocation: Silver Level for College Hires  
Sign-on Bonus: \$ [REDACTED]  
Stock: [REDACTED]  
Intended start date: [REDACTED]

RESUME:

[REDACTED]  
[REDACTED]@ [REDACTED]

Research Interests

Broad interests in robust computer system design with emphasis on efficiency and performance in various system stack abstraction layers, from computer architecture to operating systems and programming languages.

Education

July 2013 (expected): Ph.D., Department of Electrical and Computer Engineering, Carnegie Mellon University, Pittsburgh, PA, USA

Thesis: *"Lightweight and low-cost mechanisms to enable parallel monitoring of multithreaded applications"*

Advisor: Prof. Babak Falsafi

May 2011: M.Sc., Department of Electrical and Computer Engineering, Carnegie Mellon University, Pittsburgh, PA, USA

Advisor: Prof. Babak Falsafi

June 2006: M.Sc. Department of Computer Science, University of Crete, Greece.

Thesis: *"Design and implementation of a coherent memory system, including a coherent network interface, for a Power PC processor on a Xilinx Virtex II Pro FPGA"*

Advisor: Prof. Manolis Katevenis

June 2004: B.Sc. Department of Computer Science, University of Crete, Greece.

Thesis: *"Study of Asynchronous Latch Controllers for de-synchronized systems"*

Advisors: Dr. Christos Sotiriou, Prof. Manolis Katevenis

Honors

§§ December 2011 – Graduate fellowship awarded to Greek students with exceptional academic performance (Gerondelis Foundation, MA)

§§ June 2009 – August 2009: Summer Research Fellowship (Intel Labs Pittsburgh, PA)

§§ June 2008 – August 2008: Summer Research Fellowship (Intel Labs Pittsburgh, PA)

§§ September 2004 – July 2006: ICS--FORTH graduate fellowship (Greece)

§§ May 2003 – August 2004: ICS--FORTH undergraduate fellowship (Greece)

Professional Experience

September 2006 – Present: Graduate Research Assistant, Carnegie Mellon University  
June 2007 – August 2007: Intern, Intel Research Pittsburgh.  
Mentors: Dr. Michael A. Kozuch and Dr. Shimin Chen.  
May 2003 – June 2006: Research Assistant, Institute of Computer Science – Foundation  
for Research and Technology Hellas (ICS--FORTH)

#### Referred Conference and Journal Publications

1. ParaLog: Enabling and Accelerating Online Parallel Monitoring of Multithreaded Applications. [REDACTED], Michelle L. Goodstein, Michael A. Kozuch, Shimin Chen, Babak Falsafi, Phillip B. Gibbons, and Todd C. Mowry. *International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS)*, March 2010.
2. Butterfly Analysis: Adapting Dataflow Analysis to Dynamic Parallel Monitoring. Michelle L. Goodstein, [REDACTED], Shimin Chen, Phillip B. Gibbons, Michael A. Kozuch, Todd C. Mowry. *International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS)*, March 2010.
3. Flexible Hardware Acceleration for Instruction--Grain Lifeguards. Shimin Chen, Michael A. Kozuch, Phillip B. Gibbons, Michael Ryan, Theodoros Strigkos, Todd C. Mowry, Olatunji Ruwase, [REDACTED], Babak Falsafi, Vijaya Ramachandran. *IEEE Micro, Jan/Feb 2009 Special Issue: Micro's Top Picks in Computer Architecture Conferences*.
4. Flexible Hardware Acceleration for Instruction--Grain Program Monitoring. Shimin Chen, Michael A. Kozuch, Theodoros Strigkos, Babak Falsafi, Phillip B. Gibbons, Todd C. Mowry, Vijaya Ramachandran, Olatunji Ruwase, Michael Ryan and [REDACTED]. *International Symposium on Computer Architecture (ISCA)*, June 2008.

#### Under Submission

5. Resolve: Enabling Accurate Parallel Monitoring under Relaxed Memory Models. [REDACTED], Sotiria Fytraki, Michael A. Kozuch, Phillip B. Gibbons and Babak Falsafi. *Under submission*.
6. MonArch: A Flexible Single--Core Monitoring Architecture. Sotiria Fytraki, [REDACTED], Onur Kocberber, Boris Grot and Babak Falsafi. *Under submission*.

#### Workshop Publications, Technical Reports and Demo Presentations

7. Parallel LBA: Coherence--based Parallel Monitoring of Multithreaded Applications. [REDACTED], Michelle Goodstein, Michael A. Kozuch, Shimin Chen, Babak Falsafi, Phillip B. Gibbons, Todd C. Mowry, and Olatunji Ruwase. *Carnegie Mellon University Technical Report: CMU--CS--09--108, March 2009*.
8. The Butterfly Model: Theoretical Foundations. Michelle Goodstein, [REDACTED], Shimin Chen, Phillip Gibbons, Michael A. Kozuch, and Todd C. Mowry. *Carnegie Mellon University Technical Report: CMU--CS--08--170, February 2009*.
9. Log--Based Architectures: General--Purpose Program Monitoring Using Idle Cores. Shimin Chen, Babak Falsafi, Phillip B. Gibbons, Michael A. Kozuch, Todd C. Mowry, Michael Ryan, Radu Teodorescu, Anastasia Ailamaki, Limor Fix, Gregory R. Ganger, Bin Lin, Olatunji Ruwase, Theodoros Strigkos and [REDACTED]. *Intel Research Pittsburgh Tech Report TRP--08--02*
10. An FPGA--based Prototyping Platform for Research in High--Speed Interprocessor Communication. Vassilis Papaefstathiou, Giorgos Kalokairinos, Aggelos Ioannou, Michael Papamichael, Giorgos Mihelogiannakis, Stamatis Kavadias, [REDACTED], Dionisios Pnevmatikatos and Manolis Katevenis. *2<sup>nd</sup> HIPEAC Industrial Workshop, October 2006*.
11. An Asynchronous Open--Source DLX Processor. Nikolaos Andrikos, Evriklis Kounalakis, Pavlos M. Mattheakis, Christos P. Sotiriou and [REDACTED]. *Demo presented in ASYNC 2004*.

--

Liz Lee | Sr. Recruiting Program Manager  
Phone: +1 650 506 5219 | Fax: 1 650 633 1184

